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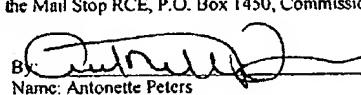
S/N 10/533489

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	TSUKADA et al.	Examiner:	INGHAM, John C.
Serial No.:	10/533489	Group Art Unit:	2814
Filed:	April 28, 2005	Docket No.:	10921.0313USWO
Title:	CHIP RESISTOR, PROCESS FOR PRODUCING THE SAME, AND FRAME FOR USE THEREIN		

**CERTIFICATE UNDER 37 CFR 1.6(d):**

I hereby certify that this paper is being transmitted by facsimile to the Mail Stop RCE, P.O. Box 1450, Commissioner for Patents, Alexandria, VA 22313-1450 on March 25, 2008.

  
By: \_\_\_\_\_  
Name: Antonette Peters**SUBMISSION UNDER 37 CFR 1.114(c)**

Mail Stop: RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Dear Sir:

As a submission required under 37 CFR 1.114(c), Applicants submit the following amendments and remarks.

**Amendments to the Claims** are reflected in the listing of claims that begins on page 2 of this paper.

**Remarks** begin on page 8 of this paper.